

ABSTRACT

High-resolution serial data can be obtained by using a costly, large-scale high-performance IC. A high resolution can be achieved without using any high-performance PLL or the like by a low-cost, simple system capable of generating a fundamental waveform on the basis of serial data synchronous with the leading and the trailing edges of a clock signal, of generating a delayed clock signals of a plurality of times by a delay device, and of superposing the fundamental waveform and the delayed clock signals.